	Subclass	ISSUE CLASSIFICATION
09/8/18/ 09/8/18/	Class	ISSUE CLA

* * *	× .	- 3.	
	 4 1:	•	

PATENT NUMBER

U.S. UTILITY Patent Application

oror orranti i atorit i ppilo	anor:		
O.I.P.E.	PATENT DATE		
SCANNED 103 O.A. AN3	. 5	. *	
			 ^~~

	DI 10 17101111					
AP	PLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
	09/818499	DF	445/3	<i>≨</i> 86	2879	1 0
STA	Shinji Ta	adaki	913	1 0,0	<u></u>	I Fr. Terry

Noriyuki Awaji Fumihiro Namiki Katsuya Irie

Plasma display panel and process for manufacturing its substrate structure

ISSUING CLASSIFICATION								
ORIGINA	AL			CROSS RI	EFERENCE(S	S)		
CLASS	SUBCLASS	CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)				K)		
INTERNATIONAL	L CLASSIFICATION							
2.0								
	· · · · · · · · · · · · · · · · · · ·							
	<u></u>							
			(405)					
					Continued on	lssue Slip Inside	File Jacket	

TERMINAL	DRAWINGS			CLAIMS ALLOWED		
☐ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent subsequent to (date)				NOTICE OF ALLOWANCE MAILED		
has been disclaimed.	(Assistant	Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date			·			
of U.S Patent. No.				ISS	UE FEE	
·				Amount Due	Date Paid	
	(Primary E	Examiner)	(Date)			
The terminalmonths of				ISSUE BATCH NUMBER		
this patent have been disclaimed.	· (Legal Instrume	ants Examiner)	(Date)			
WARNING: The Information disclosed herein may be res Possession outside the U.S. Patent & Tradem	tricted. Unauthorized nark Office is restricted	disclosure may be p	prohibited by the Up	nited States Code Title 3	5, Sections 122, 181 and 36	
orm PTO-436A	<del></del>		FILED WITH:	DISK (CRF)	FICHE CD-RC	

(Attached in pocket on right inside flap)